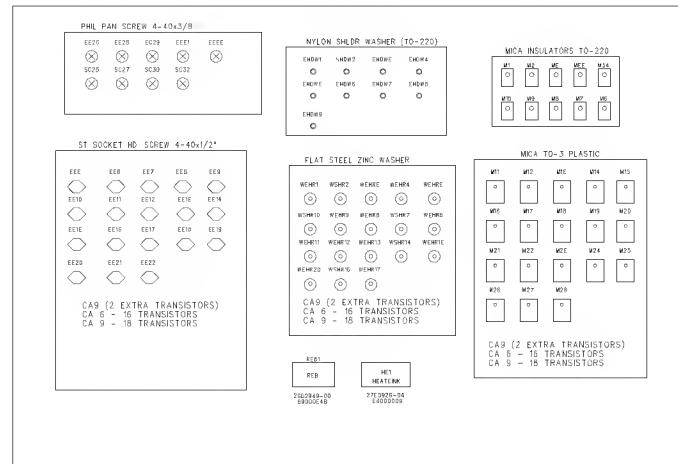


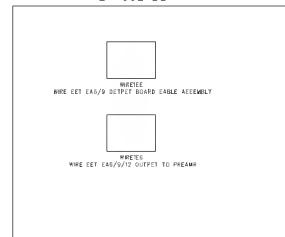
REVIEW RECORD			
LTR	E.O. NO.	APPROVED	DATE

REV-1 BOARDS WERE NOT SENT OUT  
CA 6,99 OUTPUT WAS 7001 OUTPUT PCB  
SCHEMATIC MATCHES REV-1 EXCEPT FOR VALUES  
LAYOUT WAS DONE USING OLD DATABASE  
SEVERAL RESISTOR PINS 1 & 2 ARE SWAPPED BETWEEN SCHEMATIC AND PCB  
RESISTOR PINS FIXED ON REV-1

## HEATSINK MODULE ASSY.



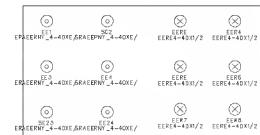
#### WIRE ASSEMBLY



FASTEN PREAMP TO OUTPUT PCB



FASTEN OUTPUT PCB TO HEATSINK



LM35D



		CRESTAUDIO			
		TITLE			
		CA 6, 9 OUTPUT MODULE			
SHAWN	DATES	ELEC	FDE	DRAWING NO.	REV
EMERED	DATES				
QUALITY CONTROL	DATES				
REFERENCE	DATES				
SCALE		SHEET 2 OF 2			